

Customer No.: 31561
Docket No.: 7558-US-PA
Application No.: 10/064,916

In the Specification:

Please amend paragraph [0012] in the specification as follow:

The present invention provides a multi-memory architecture wherein the one-time maximum accessible data storage capacity is known as the total storage capacity. The total memory of the multi-memory architecture is the same as that of a generic memory device having the same number of pins. The total number of pins refers to the all the used and unused pins of the multi-memory architecture. The multi-memory architecture of the invention is capable of providing a specific externally-accessible data storage capacity based on a predefined pin configuration including used and unused pins comprising: a first memory device having a first data storage capacity and a first predefined pin configuration having a first number of pins; and a second memory device having a second data storage capacity and a second predefined pin configuration having a second number of pins; wherein the number of first number of pins is larger than the number of second pins and the overall pin configuration of the multi-memory architecture is compatible with the first predefined pin configuration of the first memory device having the same amount of storage capacity as the multi-memory architecture.